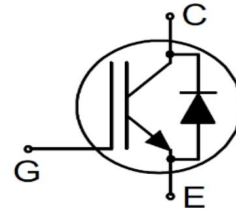




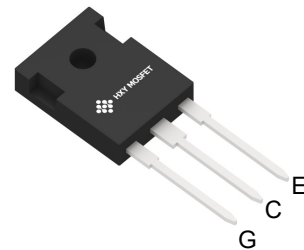
Features

- 650V, 40A IGBT
- Easy paralleling capability due to positive temperature coefficient in $V_{CE(SAT)}$
- Low EMI
- Low Gate Charge
- Low Saturation Voltage $V_{CE(SAT)}$
- Maximum junction temperature $T_{VJmax}=175^{\circ}C$



Application

- UPS
- EV-Charger
- Solar String Inverter
- Energy Storage Inverter



Key Performance and Package Parameters

Device	V_{CE}	I_C ($T_C = 25^{\circ}C$)	$V_{CE(SAT)}$ ($T_{VJ} = 25^{\circ}C$, $V_{GE} = 15V$)	V_F ($T_{VJ} = 25^{\circ}C$, $I_F = 40A$)	Package	Packing
IXYH50N65C3H1	650V	40A	1.6 V	1.8V	TO-247	30PCS

Absolute Maximum Ratings (@ $T_{VJ} = 25^{\circ}C$ unless otherwise specified)

Symbol	Parameter		Value	Units
V_{CE}	Collector emitter voltage		650	V
I_C	DC collector current ⁽¹⁾	$T_C = 25^{\circ}C$	70	A
		$T_C = 100^{\circ}C$	40	A
I_{CM}	Pulsed collector current	$T_C = 25^{\circ}C$	160	A
I_F	Maximum Diode forward current ⁽¹⁾	$T_C = 25^{\circ}C$	70	A
		$T_C = 100^{\circ}C$	40	A
I_{FM}	Diode pulsed current	$T_C = 25^{\circ}C$	160	A
V_{GE}	Gate-Emitter voltage	$T_{VJ} = 25^{\circ}C$	± 20	V
	Transient Gate-Emitter Voltage ($t_p \leq 10\mu s$, $D < 0.010$)	$T_{VJ} = 25^{\circ}C$	± 30	V
P_{tot}	Power Dissipation	$T_C = 25^{\circ}C$	250	W
		$T_C = 100^{\circ}C$	125	W
T_{VJ}	Operating Junction Temperature Range		-40 to +175	$^{\circ}C$
T_{STG}	Storage Temperature Range		-55 to +150	$^{\circ}C$



Thermal Resistance

Symbol	Parameter	Conditions	Max.	Unit
$R_{\theta JA}$	Thermal resistance: junction - ambient		40	°C/W
$R_{\theta JC}$	IGBT Thermal resistance: junction - case	IGBT	0.6	°C/W
$R_{\theta JC}$	Diode Thermal resistance: junction - case	Diode	0.65	°C/W

Electrical Characteristics (@ $T_{VJ} = 25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
V _{(BR)CES}	Collector - Emitter Breakdown Voltage	V _{GE} = 0V , I _C = 0.5mA	650	-	-	V
V _{CESAT}	Collector - Emitter Saturation Voltage	V _{GE} = 15V , I _C = 40A	-	1.6	2.1	V
		V _{GE} = 15V , I _C = 40A ,T _{VJ} = 125°C	-	1.85	-	V
		V _{GE} = 15V , I _C = 40A ,T _{VJ} = 175°C	-	1.95	-	V
V _F	Diode forward voltage	V _{GE} = 0V , I _C =40A	-	1.8	-	V
		V _{GE} = 0V , I _C =40A ,T _{VJ} = 125°C	-	1.5	-	V
		V _{GE} = 0V , I _C = 40A ,T _{VJ} = 175°C	-	1.35	-	V
V _{GE(th)}	Gate-Emitter threshold voltage	V _{GE} = V _{CE} , I _C = 250μA	3.2	4	4.8	V
I _{CES}	Zero Gate voltage Collector current	V _{CE} = 650V , V _{GE} = 0V	-	-	40	μA
I _{GES}	Gate-Emitter leakage current	V _{GE} = ±20V , V _{CE} = 0V	-	-	±100	nA
g _{fs}	Transconductance	V _{GE} =15V, I _C = 40A	-	55	-	S
Dynamic Characteristics						
C _{ies}	Input Capacitance	V _{GE} = 0V, V _{CE} = 25V, f = 1MHz	-	1520	-	pF
C _{oes}	Output Capacitance		-	110	-	pF
C _{res}	Reverse Transfer Capacitance		-	11	-	pF
Q _g	Gate Charge	V _{GE} = 0 to 15V V _{CE} = 520V, I _C = 40A	-	57	-	nC
Q _{ge}	Gate to Emitter charge		-	6.5	-	nC
Q _{gc}	Gate to Collector charge		-	17.5	-	nC
Switching Characteristics						
t _{d(on)}	Turn-On DelayTime	V _{GE} = 15V, V _{CC} = 400V I _C = 40A, R _{G(on)} =15Ω,RG _(off) =15Ω	-	26	-	ns
t _r	Turn-On Rise Time		-	28	-	ns
t _{d(off)}	Turn-Off DelayTime		-	136	-	ns
t _f	Turn-Off Fall Time		-	34	-	ns
E _{on}	Turn-on energy		-	0.9	-	mJ
E _{off}	Turn-off energy		-	0.43	-	mJ
E _{ts}	Total switching energy		-	1.33	-	mJ
Diode Recovery Characteristics						
T _{rr}	Reverse recovery time	V _R = 400 V, I _F = 40 A, di/dt = 400 A/μS	-	56	-	ns
Q _{rr}	Reverse recovery charge		-	0.27	-	μC
I _{rrm}	Peak reverse recovery current		-	8.0	-	A

Notes: 1. The max Collector current rating is package limited



Typical Performance Characteristics

Fig.1 Typical Output characteristics (25°C)

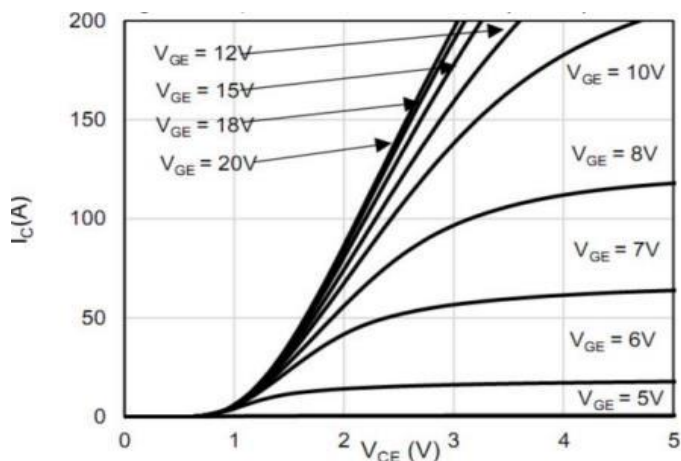


Fig.2 Typical Output characteristics (150°C)

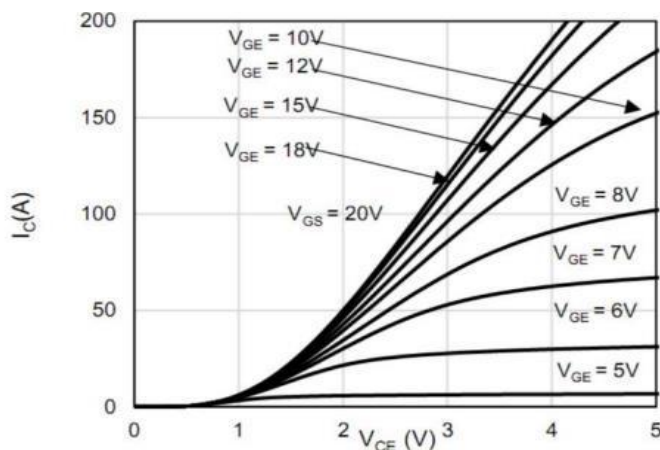


Fig.3 Forward Bias Safe Operating Area

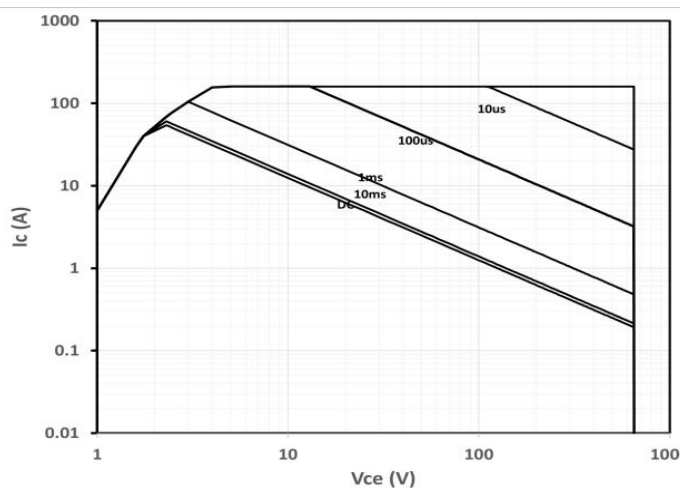


Fig.4 Transfer characteristics

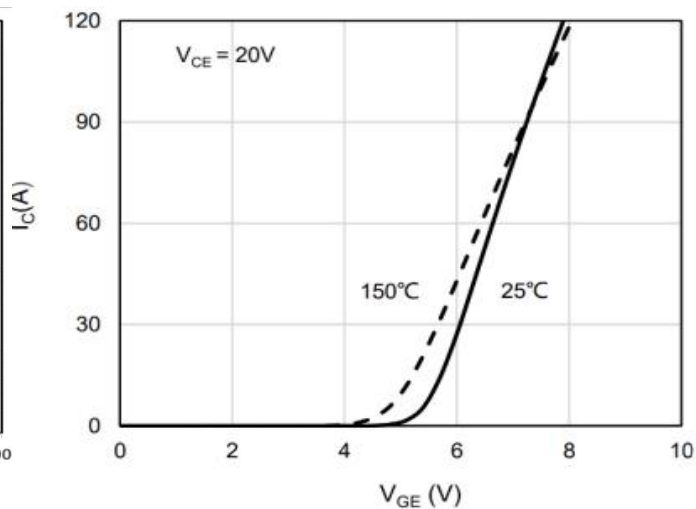


Fig.5 Gate charge characteristics

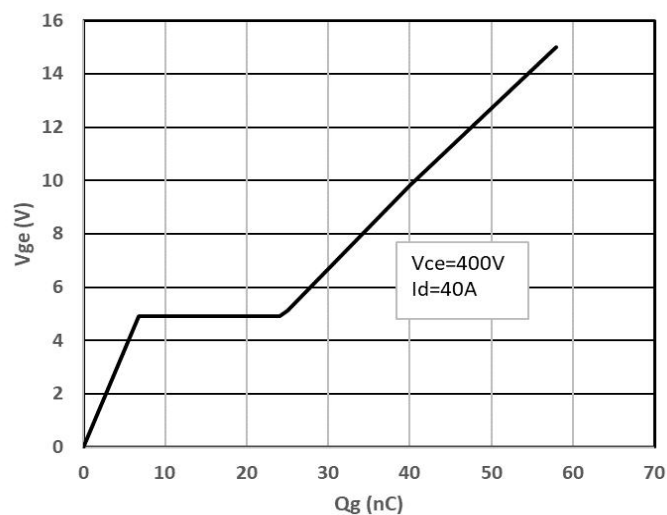


Fig.6 Typical capacitance characteristics

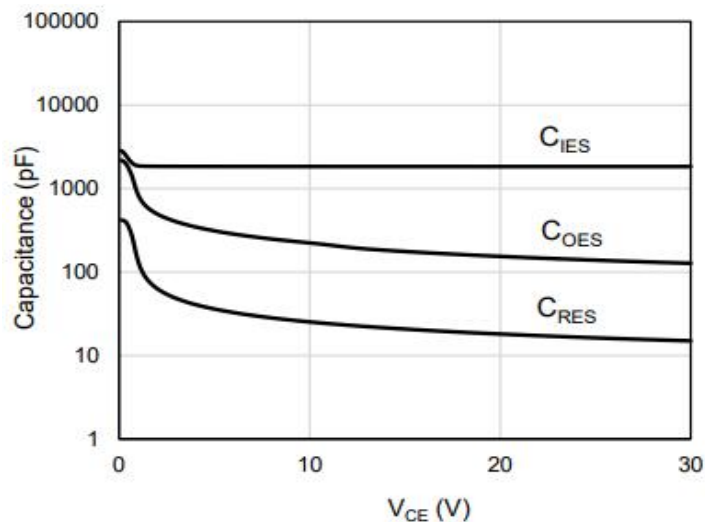




Fig.7 Vcesat vs. Junction Temperature

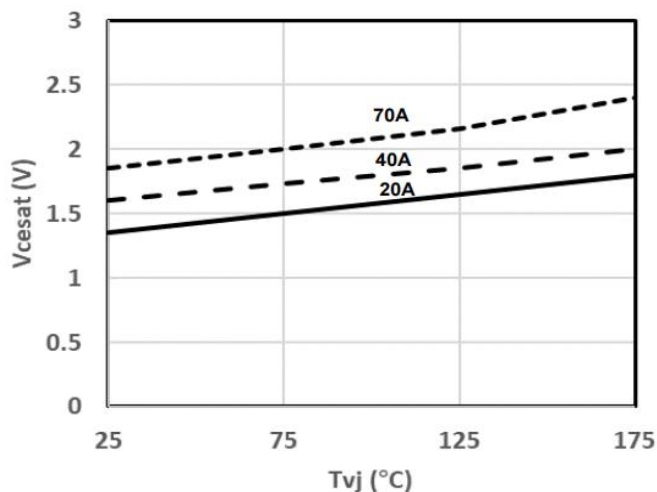


Fig.8 Typical diode VF vs. IF Characteristics

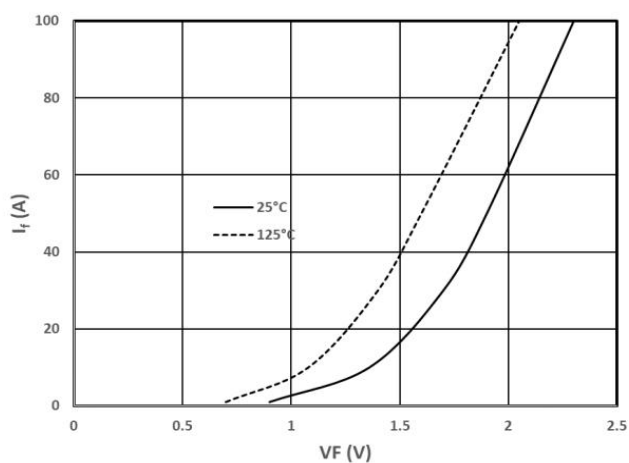


Fig.9 Threshold voltage vs. Junction temperature

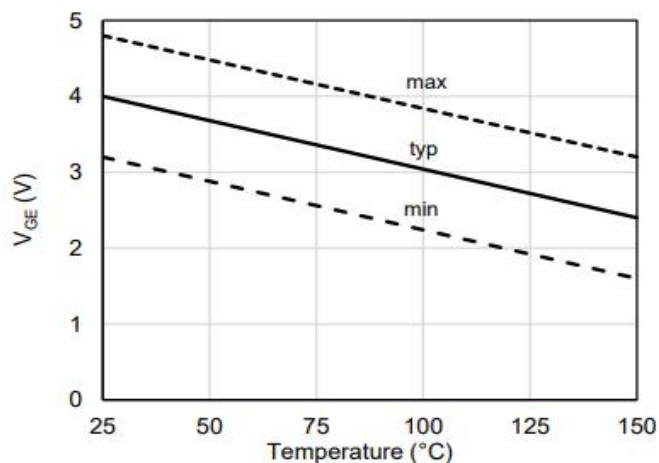


Fig.10 Transient Thermal Impedance IGBT

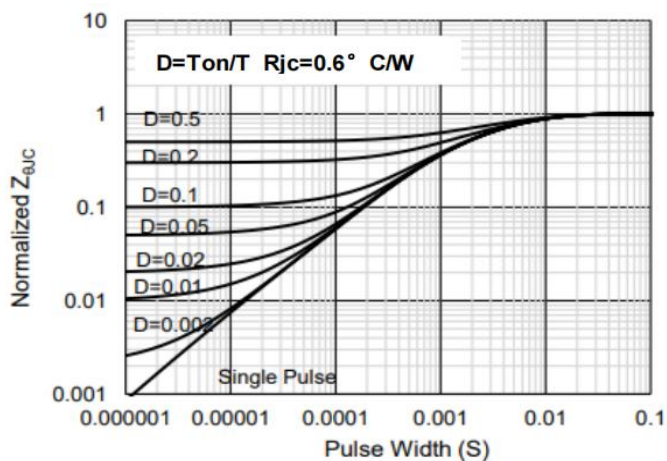
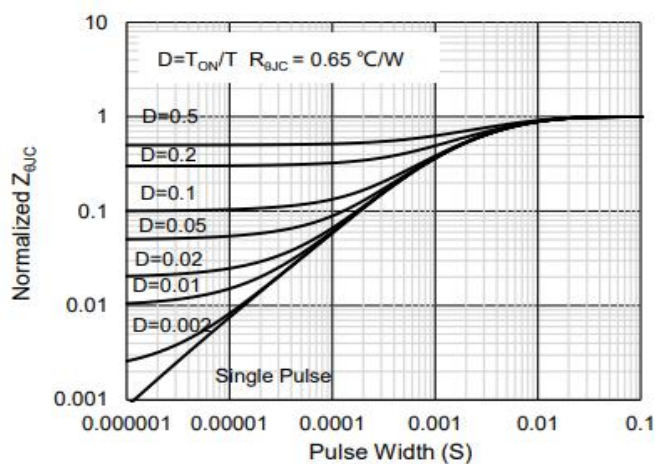
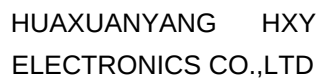


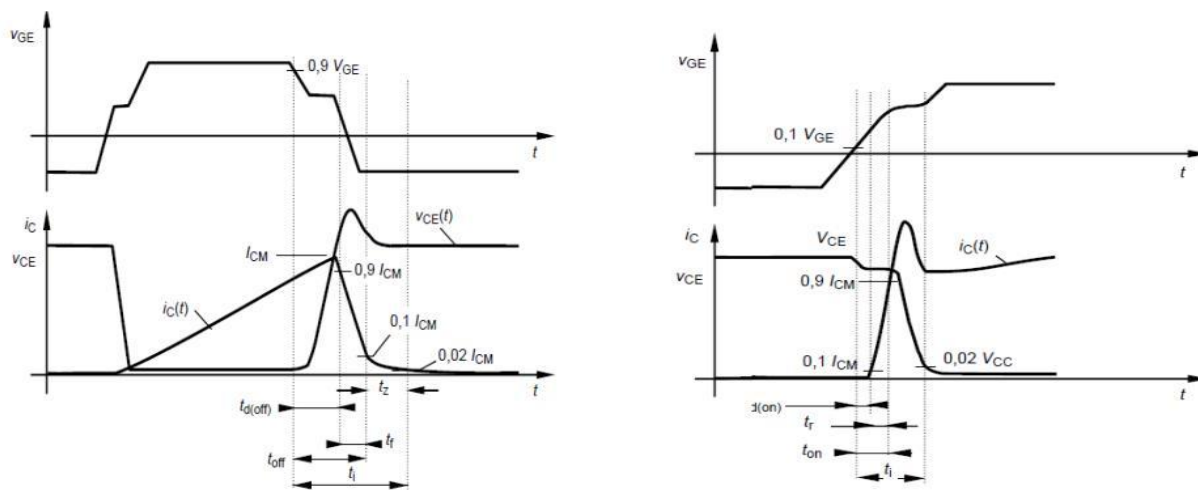
Fig.11 Transient Thermal Impedance Diode



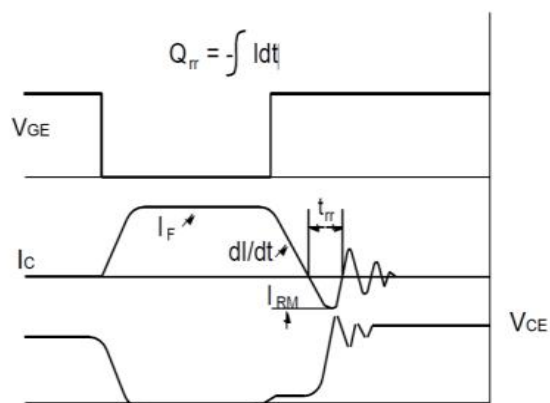


IXYH50N65C3H1
Insulated Gate Bipolar Transistor

Switching Test Circuit & Waveforms



The diagram shows a common-emitter amplifier circuit. The input signal, labeled "Double Pulse", is applied to the base of the D.U.T. (Device Under Test). The collector is connected to a load inductor L and a parallel combination of a capacitor C_{CE} and a DC voltage source V_{CE} . The output is taken from the collector, labeled I_C Monitor.





Package Mechanical Data(TO-247)



SYMBOL	mm		
	MIN	NOM	MAX
A	4.80	5.00	5.20
A1	2.21	2.41	2.59
A2	1.85	2.00	2.15
b	1.11	1.21	1.36
b2	1.91	2.01	2.21
b4	2.91	3.01	3.21
c	0.51	0.61	0.75
D	20.70	21.00	21.30
D1	16.25	16.55	16.85
E	15.50	15.80	16.10
E1	13.00	13.30	13.60
E2	4.80	5.00	5.20
E3	2.30	2.50	2.70
e	5.44BSC		
L	19.62	19.92	20.22
L1	-	-	4.30
ΦP	3.40	3.60	3.80
ΦP1	-	-	7.30
S	6.15BSC		



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