



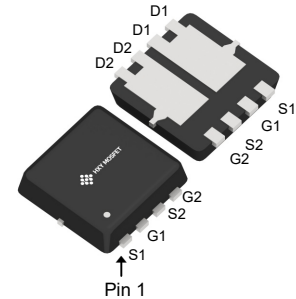
General Description

The DMT2005UDV-7 use advanced SGT MOSFET technology to provide low $R_{DS(ON)}$, low gate charge, fast switching and excellent avalanche characteristics. This device is specially designed to get better ruggedness.

General Features

$V_{DS} = 30V$ $I_D = 30A$

$R_{DS(ON)} < 6.3m\Omega @ V_{GS}=10V$



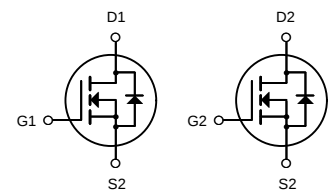
DFN3X3B-8L

Applications

Consumer electronic power supply Motor control

Synchronous-rectification Isolated DC

Synchronous-rectification applications



Dual N-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
DMT2005UDV-7	DFN3X3B-8L	HXY MOSFET	5000

Absolute Maximum Ratings at $T_A=25^{\circ}C$, unless otherwise noted

Parameter	Symbol	Value	Unit
Drain source voltage	V_{DS}	30	V
Gate source voltage	V_{GS}	± 20	V
Continuous drain current ¹⁾	$I_D @ T_c = 25^{\circ}C$	30	A
Continuous Drain Current, ¹⁾ $V_{GS} @ 10V$	$I_D @ T_c = 100^{\circ}C$	22	A
Pulsed Drain Current ²⁾	I_{DM}	135	A
Power dissipation ³⁾	P_D	30	W
Single pulsed avalanche energy ⁵⁾	E_{AS}	39.8	mJ
Avalanche Current	I_{AS}	27	A
Operation and storage temperature	T_{stg}, T_j	-55 to 150	$^{\circ}C$
Thermal resistance, junction-case	$R_{\theta JC}$	4.6	$^{\circ}C/W$
Thermal resistance, junction-ambient ⁴⁾	$R_{\theta JA}$	50	$^{\circ}C/W$



Electrical Characteristics (T_J=25°C , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	30	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =20A	---	5	6.3	mΩ
		V _{GS} =4.5V , I _D =15A	---	6.9	9	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.2	---	2.5	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =24V , V _{GS} =0V , T _J =25°C	---	---	1	uA
		V _{DS} =24V , V _{GS} =0V , T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V , I _D =20A	---	67	---	S
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz	---	1.7	---	Ω
Q _g	Total Gate Charge (4.5V)	V _{DS} =15V , V _{GS} =4.5V , I _D =15A	---	8	---	nC
Q _{gs}	Gate-Source Charge		---	2.4	---	
Q _{gd}	Gate-Drain Charge		---	3.2	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =15V , V _{GS} =10V , R _G =3.3Ω I _D =15A	---	7.1	---	ns
T _r	Rise Time		---	40	---	
T _{d(off)}	Turn-Off Delay Time		---	15	---	
T _f	Fall Time		---	6	---	
C _{iss}	Input Capacitance	V _{DS} =15V , V _{GS} =0V , f=1MHz	---	814	---	pF
C _{oss}	Output Capacitance		---	208	---	
C _{rss}	Reverse Transfer Capacitance		---	201	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,6}	V _G =V _D =0V , Force Current	---	---	30	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25°C	---	---	1	V
t _{rr}	Reverse Recovery Time	I _F =20A , di/dt=100A/μs ,	---	15	---	nS
Q _{rr}	Reverse Recovery Charge	T _J =25°C	---	25	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V_{DD}=25V,V_{GS}=10V,L=0.1mH,I_{AS}=24A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.



Typical Characteristics

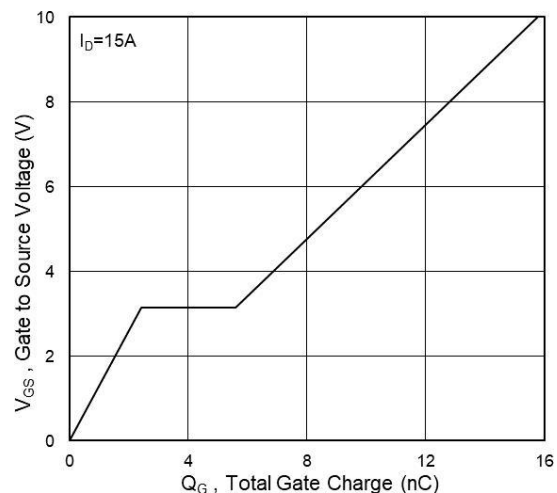
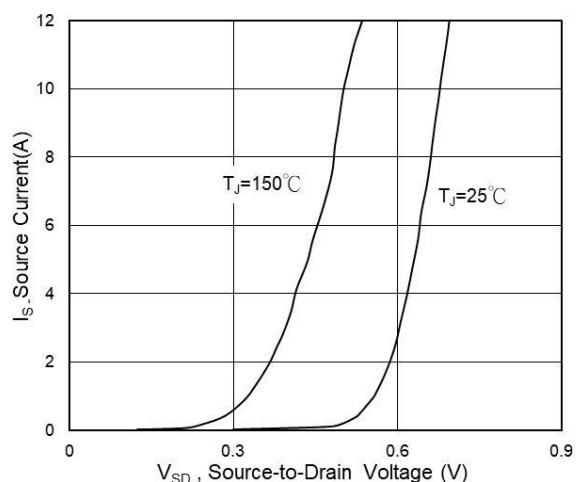
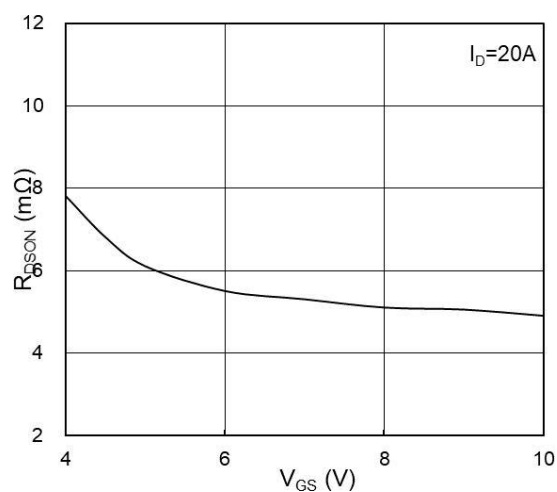
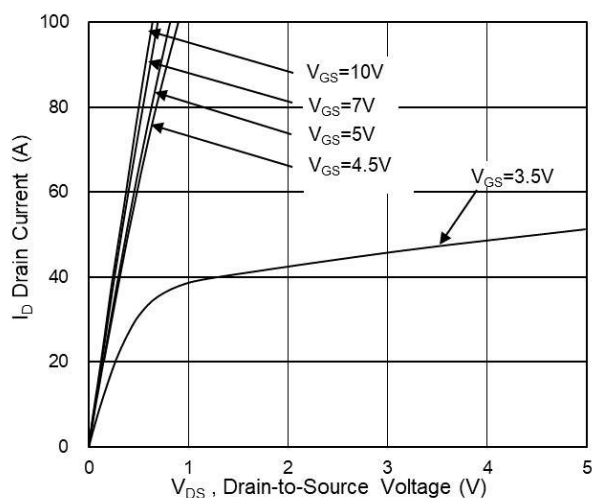
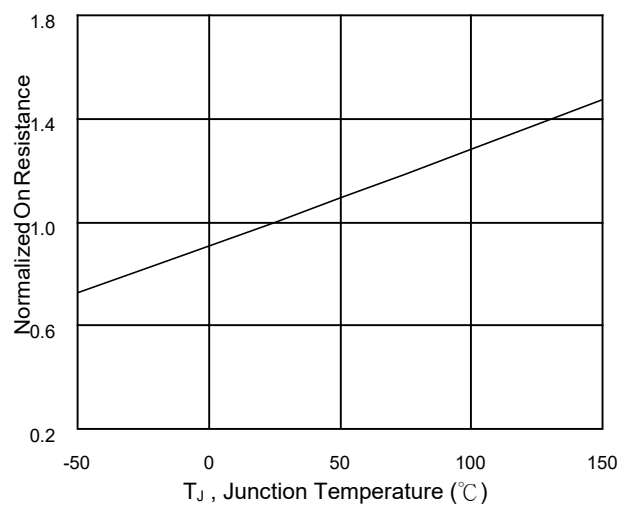
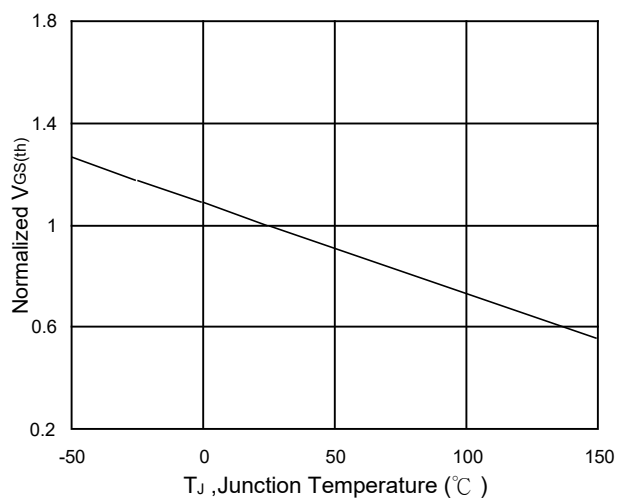


Fig.3 Source Drain Forward Characteristics

Fig.4 Gate-Charge Characteristics



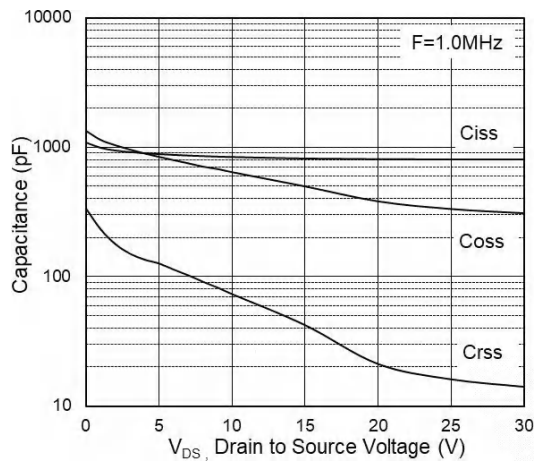


Fig.7 Capacitance

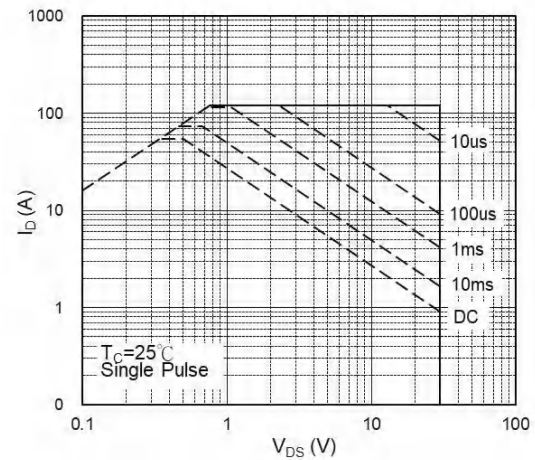


Fig.8 Safe Operating Area

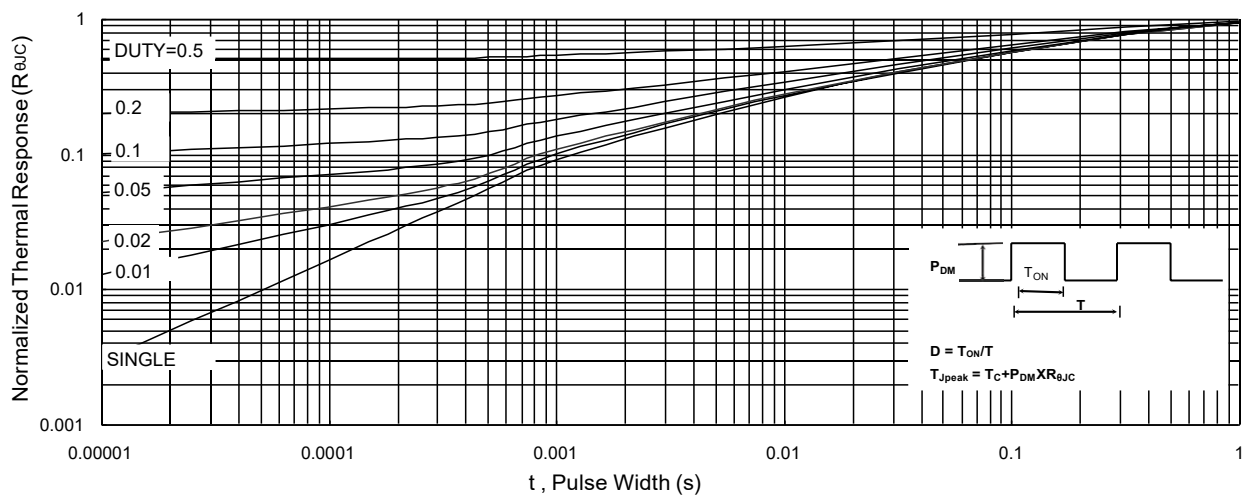


Fig.9 Normalized Maximum Transient Thermal Impedance

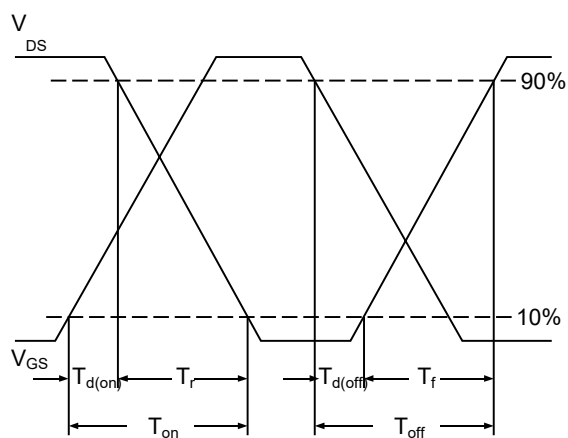


Fig.10 Switching Time Waveform

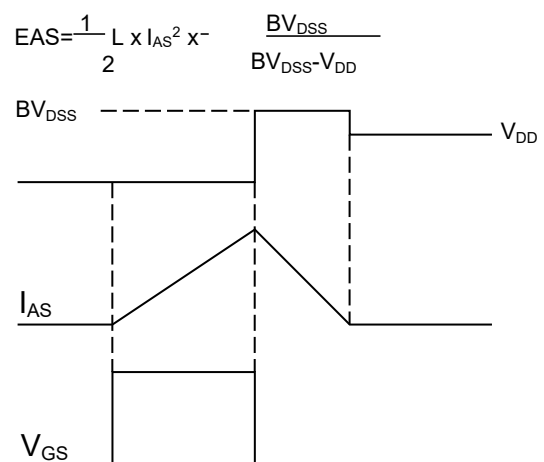
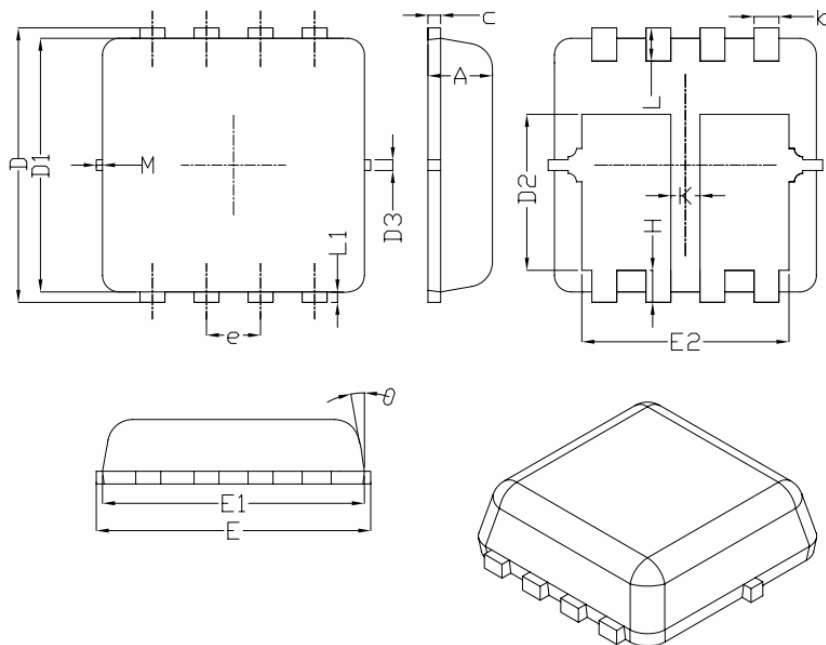


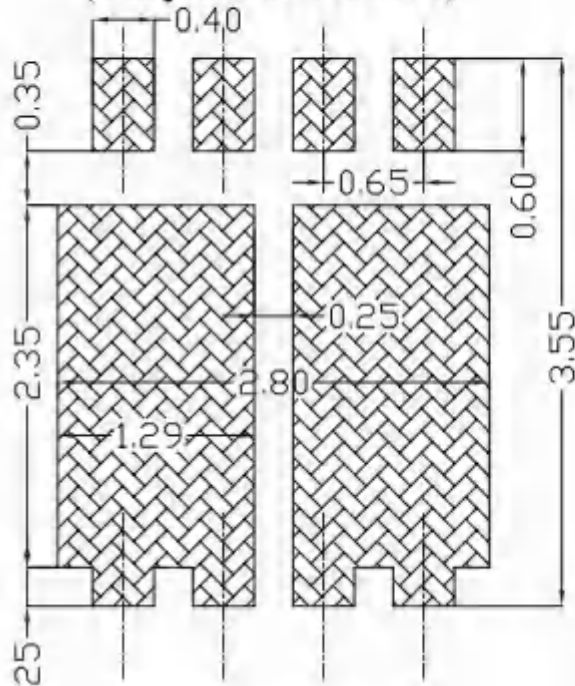
Fig.11 Unclamped Inductive Switching Waveform



DFN3X3-8L Package Information



Land Pattern
(Only for Reference)



SYMBOL	DIMENSIONAL REOMTS		
	MIN	NOM	MAX
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.78	1.88	1.98
D3	---	0.13	---
E	3.20	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	---	0.13	---
K	0.30	---	---
θ	---	10°	12°
M	*	*	0.15
* Not specified			



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