

#### **Description**

The DMN2320UFB4-7B uses advanced trench totechnology provide excellent R  $_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

# DFN1006-3L (X2-DFN1006-3)

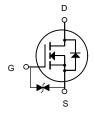
#### **General Features**

 $V_{DS} = 20V I_{D} = 0.7A$ 

 $R_{DS(ON)}$  < 350 m $\Omega$ @  $V_{GS}$ =4.5V

 $R_{DS(ON)}$  < 420 m $\Omega$ @  $V_{GS}$ =2.5V

ESD=2500V HBM



#### N-Channel MOSFET

#### **Application**

Load/Power Switching
Interfacing Switching
Battery Management for Ultra Small Portable Electronics

#### **Package Marking and Ordering Information**

Product ID	Pack	Brand	Qty(PCS)
DMN2320UFB4-7B	DFN1006-3L(X2-DFN100-3)	HXY MOSFET	10000

### Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Limit	Unit
V <sub>D</sub> S	Drain-Source Voltage	20	V
Vgs	Gate-Source Voltage	±10	V
I <sub>D</sub>	Drain Current-Continuous	0.7	Α
P <sub>D</sub>	Maximum Power Dissipation	0.15	W
Тл,Тѕтс	Operating Junction and Storage Temperature Range	-55 To 150	$^{\circ}$
Reja	Thermal Resistance,Junction-to-Ambient (Note 2)	1250	°C/W

#### N-Channel Enhancement Mode MOSFET

## Electrical Characteristics (TA= 25°C, unless otherwise specified)

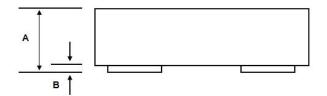
Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> =250μA	20			V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> =20V,V <sub>GS</sub> = 0V			1	μA
Gate-body leakage current	Igss	V <sub>GS</sub> =±10V, V <sub>DS</sub> = 0V			±10	μA
Gate threshold voltage (note2)	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	0.45	0.7	1.1	V
Drain-source on-resistance (note2)		V <sub>GS</sub> =4.5V, I <sub>D</sub> =0.5A		0.22	0.35	Ω
	R <sub>DS(on)</sub>	V <sub>GS</sub> =2.5V, I <sub>D</sub> =0.5A		0.28	0.42	Ω
Forward tranconductance (note2)	<b>g</b> fs	V <sub>DS</sub> =5.0V, I <sub>D</sub> =0.5A		1.6		S
Diode forward voltage	V <sub>SD</sub>	I <sub>S</sub> =0.8A, V <sub>GS</sub> =0V			1.2	V
Dynamic Characteristics (note4)						
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> =10V,V <sub>GS</sub> =0V, f =1MHz		43.6		pF
Output capacitance	Coss			6.8		pF
Reverse transfer capacitance	C <sub>rss</sub>	1 11/11/2		4.6		pF
Switching Characteristics <sub>(note4)</sub>	·					
Turn-on delay time (note3)	t <sub>d(on)</sub>	$V_{GS}$ =4.5V, $V_{DS}$ =10V, $R_L$ =20 $\Omega$		1.4		nS
Turn-on rise time (note3)	t <sub>r</sub>			27.8		nS
Turn-off delay time (note3)	t <sub>d(off)</sub>			54.6		nS
Turn-off fall time (note3)	t <sub>f</sub>			25.6		nS

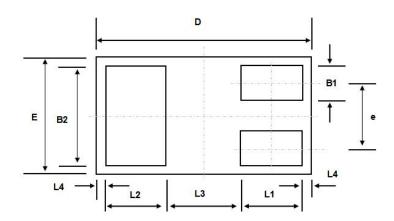
#### Notes:

- 1. Surface mounted on FR4 board using the minimum recommended pad size.
- 2. Pulse Test : Pulse Width=300µs, Duty Cycle=2%.
- 3. Switching characteristics are independent of operating junction temperatures.
- 4. Guaranteed by design, not subject to producting.



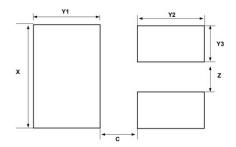
# Package Outline Dimensions DFN1006-3L(X2-DFN1006-3)





	Dimensions In			
Symbol	Millimet			
	Min	Max		
Α	0.33	0.50		
В	0.00	0.05		
B1	0.10	0.20		
B2	0.45	0.55		
D	0.90	1.05		
Е	0.50	0.65		
е	0.3	5		
L1	0.20	0.30		
L2	0.20	0.30		
L3	0.39			
L4	0.	05		

# Suggested Pad Layout (mm)



Symbol	Dimensions
С	0.25
Х	0.65
Y1	0.50
Y2	0.50
Y3	0.25
Z	0.20

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